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Claims

WHAT IS CLAIMED IS:

1	A		
1.	Α	system,	comprising:

a computer for:

according to a first model of an operation of circuitry, generating a first set of estimates of the operation in response to a set of conditions, including a first estimate of the operation in response to a first condition;

according to a second model of the operation, generating a second set of estimates of the operation in response to the first condition and the first set;

in response to a comparison between the first estimate and the second set, selecting a subset of the first set; and

according to the second model, generating an estimate of the operation in response to a second condition and the selected subset.

- 2. The system of Claim 1 wherein the first model includes a circuit simulator.
- 3. The system of Claim 1 wherein the second model includes a characteristic equation.
- 4. The system of Claim 3 wherein the second model includes a characterization table that includes the characteristic equation.
- 5. The system of Claim 3 wherein the second model is a static timing analysis model including the characteristic equation.
- 6. The system of Claim 3 wherein the computer is for generating the second set by:

 determining respective sets of constant elements of the characteristic equation in response

 to subsets of the first set; and
 - according to the characteristic equation, in response to the first condition, generating the second set including respective estimates of the operation in response to the sets of constant elements.

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- 7. The system of Claim 6 wherein the computer is for selecting the selected subset from among the subsets of the first set in response to the comparison, the comparison being a comparison between the first estimate and the estimates of the second set.
 - 8. The system of Claim 7 wherein:

the subsets of the first set are respectively associated with the sets of constant elements, so the selected subset of the first set is associated with a particular set of constant elements;

the estimates of the second set are respectively associated with the sets of constant elements, so a particular estimate of the second set is associated with the particular set of constant elements; and

among the estimates of the second set, the particular estimate is closest to the first estimate.

- 9. The system of Claim 1 wherein the operation is a response time of the circuitry.
- 10. The system of Claim 1 wherein the operation is a propagation delay of the circuitry.
- 11. The system of Claim 1 wherein the conditions include at least two types of conditions.
- 12. The system of Claim 1 wherein the conditions include at least three types of conditions.
- 1 13. The system of Claim 1 wherein the conditions include capacitive loadings of the circuitry.
- 1 14. The system of Claim 1 wherein the conditions include input transition times of the circuitry.
- 1 15. The system of Claim 14 wherein the conditions include at least two types of input transition times of the circuitry.
 - 16. The system of Claim 1 wherein the circuitry is integrated circuitry.

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17	Α	method.	comprising
11.	* Y	11100000	OULLIPATORILE,

with a first computer-implemented model of an operation of circuitry, generating a first set of estimates of the operation in response to a set of conditions, including a first estimate of the operation in response to a first condition;

with a second computer-implemented model of the operation, generating a second set of estimates of the operation in response to the first condition and the first set;

in response to a comparison between the first estimate and the second set, selecting a subset of the first set; and

with the second computer-implemented model, generating an estimate of the operation in response to a second condition and the selected subset.

18. The method of Claim 17 wherein the generating of the first set of estimates comprises:

with the first computer-implemented model of the operation, generating the first set of estimates of the operation in response to the set of conditions, the first computer-implemented model including a circuit simulator.

19. The method of Claim 17 wherein the generating of the second set of estimates comprises:

with the second computer-implemented model of the operation, generating the second set of estimates, the second computer-implemented model including a characteristic equation.

20. The method of Claim 20 wherein the generating of the second set of estimates comprises:

with the second computer-implemented model of the operation, generating the second set of estimates, the second computer-implemented model including a characterization table that includes the characteristic equation.

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first estimate.

1	21. The method of Claim 20 wherein the generating of the second set of estimates
2	comprises:
3	with the second computer-implemented model of the operation, generating the second set
. 4	of estimates, the second computer-implemented model being a static timing analysis model
5	including the characteristic equation.
1	22. The method of Claim 20 wherein the generating of the second set of estimates
2	comprises:
3	determining respective sets of constant elements of the characteristic equation in response
4	to subsets of the first set; and
5	according to the characteristic equation, in response to the first condition, generating the
6	second set including respective estimates of the operation in response to the sets of constant
6 7 1 2 2 3	elements.
	23. The method of Claim 22 wherein the selecting of the subset comprises:
] j2	selecting the selected subset from among the subsets of the first set in response to the
3	comparison, the comparison being a comparison between the first estimate and the estimates of
1 1 2	the second set.
j 1	24. The method of Claim 23 wherein the selecting of the subset comprises:
<u>.</u> 2	selecting the selected subset from among the subsets of the first set in response to the
3	comparison,
4	the subsets of the first set being respectively associated with the sets of constant
5	elements, so the selected subset of the first set is associated with a particular set of
6	constant elements;
7	the estimates of the second set being respectively associated with the sets of
8	constant elements, so a particular estimate of the second set is associated with the
9	particular set of constant elements; and
10	among the estimates of the second set, the particular estimate being closest to the

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25.	The method of Claim 17 wh	erein the generating of	of the first set of estimate	ÈS.
comprises:				

with the first computer-implemented model of the operation, generating the first set of estimates of the operation in response to the set of conditions, the operation being a response time of the circuitry.

26. The method of Claim 17 wherein the generating of the first set of estimates comprises:

with the first computer-implemented model of the operation, generating the first set of estimates of the operation in response to the set of conditions, the operation being a propagation delay of the circuitry.

27. The method of Claim 17 wherein the generating of the first set of estimates comprises:

with the first computer-implemented model of the operation, generating the first set of estimates of the operation in response to the set of conditions, the conditions including at least two types of conditions.

28. The method of Claim 17 wherein the generating of the first set of estimates comprises:

with the first computer-implemented model of the operation, generating the first set of estimates of the operation in response to the set of conditions, the conditions including at least three types of conditions.

29. The method of Claim 17 wherein the generating of the first set of estimates comprises:

with the first computer-implemented model of the operation, generating the first set of estimates of the operation in response to the set of conditions, the conditions including capacitive loadings of the circuitry.

30.	The method of Claim 1	7 wherein th	e generating	of the fir	rst set of	estimates
comprises:						

with the first computer-implemented model of the operation, generating the first set of estimates of the operation in response to the set of conditions, the conditions including input transition times of the circuitry.

31. The method of Claim 30 wherein the generating of the first set of estimates comprises:

with the first computer-implemented model of the operation, generating the first set of estimates of the operation in response to the set of conditions, the conditions including at least two types of input transition times of the circuitry.

32. The method of Claim 17 wherein the generating of the first set of estimates comprises:

with the first computer-implemented model of the operation, generating the first set of estimates of the operation in response to the set of conditions, the circuitry being integrated circuitry.

second set including respective estimates of the operation in response to the sets of constant

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- 39. The computer program product of Claim 38 wherein the computer application is processable by the computer for causing the computer to select the selected subset from among the subsets of the first set in response to the comparison, the comparison being a comparison between the first estimate and the estimates of the second set.
 - 40. The computer program product of Claim 39 wherein:

the subsets of the first set are respectively associated with the sets of constant elements, so the selected subset of the first set is associated with a particular set of constant elements;

the estimates of the second set are respectively associated with the sets of constant elements, so a particular estimate of the second set is associated with the particular set of constant elements; and

among the estimates of the second set, the particular estimate is closest to the first estimate.

- 41. The computer program product of Claim 33 wherein the operation is a response time of the circuitry.
- 42. The computer program product of Claim 33 wherein the operation is a propagation delay of the circuitry.
- 43. The computer program product of Claim 33 wherein the conditions include at least two types of conditions.
- 1 44. The computer program product of Claim 33 wherein the conditions include at least 2 three types of conditions.
- 45. The computer program product of Claim 33 wherein the conditions include capacitive loadings of the circuitry.
- 1 46. The computer program product of Claim 33 wherein the conditions include input 2 transition times of the circuitry.

- 1 47. The computer program product of Claim 46 wherein the conditions include at least
- 2 two types of input transition times of the circuitry.
- 1 48. The computer program product of Claim 33 wherein the circuitry is integrated
- 2 circuitry.